**APARNA PAURANIK**

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**Career Objective:**

As a bright ambitious person, I enjoy working in a fast paced, highly motivating position where I can assist others while challenging and expanding my knowledge and understanding of the task at hand. I am seeking a position that will utilize my skills and offer the chance for advancement as well as allow me the opportunity to gain additional skills and experience

**Professional Qualification:**

July 2011- till Date

At present i am working in Infotech Enterprises Ltd. as Design Engineer. I have 2.8 years of rich experience in analog and digital circuit design & analysis, component management, cost reduction and innovation in medical devices. During projects I have been a part of process from concept stage to production stage.

**Responsibilities:**

* Obsolescence management and Cost Reduction: identifying replacement for the obsolete Electronic and electrical components, reducing existing PCBA cost by replacing existing components/ material with equivalent alternate of reduced cost and maintaining quality.
* Redesign of Old PCBA to new PCBA, to improve design performance and efficiency of existing product.
* Harness modification by adding Fuse to qualify for UL compliance.
* Replace custom made switch by redesigning circuit with DPDT switch and reducing cost to 95%.
* Research and developed new ideas for upgrading existing product.
* Develop and maintain project plans using MS project.
* Schematic design and Gerber update.
* Worked on project on UL and RoHS compliance.
* Communication with cross functional teams in India and abroad for project.
* Design support at all stages of design project.
* Bare board, functionality testing of PCBAs and preparing test plan and report with result analysis. First article inspection.
* Worked on Blood bank and human sample preservation refrigerator and freezers.
* Worked on Centrifugal evaporator.
* Medical devices: Incubators and Oven.

**Area of Expertise:**

* Analog and digital circuit design and analysis.
* Board design using sensors and Led and analysis of data obtained from sensor.
* Schematic and PCB design with IPC Standard.
* Testing of bare PCB and functional testing of Boards and result analysis.
* Simulation of circuit to meet design objective.

**Educational Qualification:**

Bachelor of Engineering honours in Electronics and Communication engineering from college GGITM, Bhopal affiliated to RGPV University in 2011.

Intermediate: 71%

Carmel convent senior secondary school Bhopal

High school: 78.6%

Carmel convent senior secondary school Bhopal

**Tools:**

LT spice, MS Visio (for flow charts), Microsoft silver light, MS project

**Software:**

CAM350, PADS, DX designer

**Language:**

C and C++, SQL

**Elementary knowledge:**

Xilinx, FEKO

**Regularity Standards:**

UL 61010, RoHS (EU 2011 directive), CE